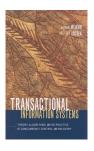
Transactional Information Systems:

Theory, Algorithms, and the Practice of Concurrency Control and Recovery

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"Teamwork is essential. It allows you to blame someone else." (Anonymous)

Part II: Concurrency Control

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Chapter 3: Concurrency Control – Notions of Correctness for the Page Model

• 3.2 Canonical Synchronization Problems

- 3.3 Syntax of Histories and Schedules
- 3.4 Correctness of Histories and Schedules
- 3.5 Herbrand Semantics of Schedules
- 3.6 Final-State Serializability
- 3.7 View Serializability
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- 3.9 Commit Serializability
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- 3.11 Lessons Learned

[&]quot;Nothing is as practical as a good theory." (Albert Einstein)

Lost Update Problem

P1	Time	P2
r (x) x := x+100 w (x)	/* x = 100 */ 1 2 4 5 /* x = 200 */ 6 /* x = 300 */	r(x) $x := x + 200$ $w(x)$



Lost Update Problem

P1	Time	P2
r (x) x := x+100 w (x)	/* x = 100 */ 1 2 4 5 /* x = 200 */ 6 /* x = 300 */	r (x) x := x+200 w (x)

fupdate "lost"

Observation: problem is the interleaving $r_1(x)$ $r_2(x)$ $w_1(x)$ $w_2(x)$

Inconsistent Read Problem

P1	Time	P2
sum := 0 r (x) r (y) sum := sum +x sum := sum + y	1 2 3 4 5 6 7 8 9 10	$r(x) \\ x := x - 10 \\ w(x)$ $r(y) \\ y := y + 10 \\ w(y)$



[&]quot;sees" wrong sum

Inconsistent Read Problem

P1	Time	P2
sum := 0 r (x) r (y) sum := sum +x sum := sum + y	1 2 3 4 5 6 7 8 9 10	$r(x) \\ x := x - 10 \\ w(x)$ $r(y) \\ y := y + 10 \\ w(y)$



[&]quot;sees" wrong sum

Observations:

problem is the interleaving $r_2(x)$ $w_2(x)$ $r_1(x)$ $r_1(y)$ $r_2(y)$ $w_2(y)$ no problem with sequential execution

Dirty Read Problem

P1	Time	P2
r (x) x := x + 100 w (x)	1 2 3	
	4 5	$\begin{array}{c} \mathbf{r} \ (\mathbf{x}) \\ \mathbf{x} := \mathbf{x} - 100 \end{array}$
failure & rollback	6 7	w (x)

cannot rely on validity of previously read data

Dirty Read Problem

P1	Time	P2
$\mathbf{r} \overset{\mathbf{(x)}}{\mathbf{x}} = \mathbf{x} + 100$	1 2	
w (x)	3 4 5	$ \begin{array}{l} \mathbf{r} (\mathbf{x}) \\ \mathbf{x} := \mathbf{x} - 100 \end{array} $
failure & rollback	6 7	w (x)

cannot rely on validity of previously read data

Observation: transaction rollbacks could affect concurrent transactions

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Schedules and Histories

Definition 3.1 (Schedules and histories):

Let $T=\{t_1, ..., t_n\}$ be a set of transactions, where each $t_i \in T$ has the form $t_i=(op_i, \leq_i)$ with op_i denoting the operations of t_i and \leq_i their ordering.

- (i) A **history** for T is a pair $s=(op(s), \leq_s) s.t.$
 - $\text{(a) op(s)} \ \subseteq \ \cup_{\scriptscriptstyle i=1..n} \ \text{op}_i \ \cup \ \cup_{\scriptscriptstyle i=1..n} \ \{a_i, \, c_i\}$
 - (b) for all i, $1 \le n$: $c_i \in op(s) \Leftrightarrow a_i \notin op(s)$
 - $(c) \cup_{i=1..n} <_i \subseteq <_s$
 - (d) for all i, $1 \le 1$, and all $p \in op_i$: $p \le c_i$ or $p \le a_i$
 - (e) for all p, $q \in op(s)$ s.t. at least one of them is a write and both access the same data item: $p <_s q$ or $q <_s p$
- (ii) A schedule is a prefix of a history.

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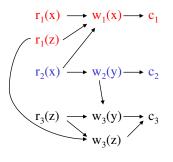
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- (ii) A schedule is a prefix of a history.

Definition 3.2 (Serial history):

A history s is **serial** if for any two transactions t_i and t_j in s, where $i \neq j$, all operations from t_i are ordered in s before all operations from t_i or vice versa.

Example Schedules and Notation

Example 3.4:



```
\begin{aligned} & trans(s) \text{:=} \\ & \{t_i \mid s \text{ contains step of } t_i \} \\ & commit(s) \text{:=} \\ & \{t_i \in trans(s) \mid c_i \in s \} \\ & abort(s) \text{:=} \\ & \{t_i \in trans(s) \mid a_i \in s \} \\ & active(s) \text{:=} \\ & trans(s) - (commit(s) \cup abort(s)) \end{aligned}
```

Example 3.6:

$$r_1(x) r_2(z) r_3(x) w_2(x) w_1(x) r_3(y) r_1(y) w_1(y) w_2(z) w_3(z) c_1 a_3$$

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Correctness of Schedules

- 1. Define equivalence relation ≈ 80 set S of all schedules.
- 2. "Good" schedules are those in the equivalence classes of serial schedules.
- Equivalence must be efficiently decidable.
- "Good" equivalence classes should be "sufficiently large".

For the moment, disregard aborts: assume that all transactions are committed.

Activity

• What is an equivalence relation?

• List the three defining conditions!

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Herbrand Semantics of Schedules

Definition 3.3 (Herbrand Semantics of Steps):

For schedule s the **Herbrand semantics H**_s of steps $r_i(x)$, $w_i(x) \in op(s)$ is:

- (i) $H_s[r_i(x)] := H_s[w_i(x)]$ where $w_i(x)$ is the last write on x in s before $r_i(x)$.
- (ii) $H_s[w_i(x)] := f_{ix}(H_s[r_i(y_1)], ..., H_s[r_i(y_m)])$ where the $r_i(y_j)$, $1 \le m$, are all read operations of t_i that occcur in s before $w_i(x)$ and f_{ix} is an uninterpreted m-ary function symbol.

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Definition 3.4 (Herbrand Universe):

For data items $D=\{x, y, z, ...\}$ and transactions t_i , $1 \le n$,

the Herbrand universe HU is the smallest set of symbols s.t.

- (i) $f_{0x}() \in HU$ for each $x \in D$ where f_{0x} is a constant, and
- (ii) if $w_i(x) \in op_i$ for some t_i , there are m read operations $r_i(y_1), ..., r_i(y_m)$ that precede $w_i(x)$ in t_i , and $v_1, ..., v_m \in HU$, then $f_{ix}(v_1, ..., v_m) \in HU$.

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Definition 3.5 (Schedule Semantics):

The Herbrand semantics of a schedule s is the mapping

 $H[s]: D \rightarrow HU$ defined by $H[s](x) := H_s[w_i(x)],$

where $w_i(x)$ is the last operation from s writing x, for each $x \in D$.

Herbrand Semantics: Example

$$s = w_0(x) w_0(y) c_0 r_1(x) r_2(y) w_2(x) w_1(y) c_2 c_1$$

$$\begin{split} &H_s[\textbf{w}_0(\textbf{x})] = f_{0x}(\) \\ &H_s[\textbf{w}_0(\textbf{y})] = f_{0y}(\) \\ &H_s[\textbf{r}_1(\textbf{x})] = H_s[\textbf{w}_0(\textbf{x})] = f_{0x}(\) \\ &H_s[\textbf{r}_2(\textbf{y})] = H_s[\textbf{w}_0(\textbf{y})] = f_{0y}(\) \\ &H_s[\textbf{w}_2(\textbf{x})] = f_{2x}(H_s[\textbf{r}_2(\textbf{y})]) = f_{2x}(f_{0y}(\)) \\ &H_s[\textbf{w}_1(\textbf{y})] = f_{1y}(H_s[\textbf{r}_1(\textbf{x})]) = f_{1y}(f_{0x}(\)) \end{split}$$

$$\begin{split} &H[s](x) = H_s[w_2(x)] = f_{2x}(f_{0y}(\)) \\ &H[s](y) = H_s[w_1(y)] = f_{1y}(f_{0x}(\)) \end{split}$$

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Final-State Equivalence

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Schedules s and s' are called **final state equivalent**, denoted $s \approx_f s'$, if op(s)=op(s') and H[s]=H[s'].

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Example a:

$$\begin{array}{l} s = r_{1}(x) \; r_{2}(y) \; w_{1}(y) \; r_{3}(z) \; w_{3}(z) \; r_{2}(x) \; w_{2}(z) \; w_{1}(x) \\ s' = r_{3}(z) \; w_{3}(z) \; r_{2}(y) \; r_{2}(x) \; w_{2}(z) \; r_{1}(x) \; w_{1}(y) \; w_{1}(x) \\ H[s](x) = H_{s}[w_{1}(x)] = f_{1s}(f_{0s}(\cdot)) = H_{s'}[w_{1}(x)] = H[s'](x) \\ H[s](y) = H_{s}[w_{1}(y)] = f_{1y}(f_{0s}(\cdot)) = H_{s'}[w_{1}(y)] = H[s'](y) \\ H[s](z) = H_{s}[w_{2}(z)] = f_{2z}(f_{0s}(\cdot), f_{0y}(\cdot)) = H_{s'}[w_{2}(z)] = H[s'](z) \end{array}$$

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Schedules s and s' are called **final state equivalent**, denoted $s \approx_{\hat{t}} s'$, if op(s)=op(s') and H[s]=H[s'].

Example a:

$$\begin{array}{l} s = r_1(x) \; r_2(y) \; w_1(y) \; r_3(z) \; w_3(z) \; r_2(x) \; w_2(z) \; w_1(x) \\ s' = r_3(z) \; w_3(z) \; r_2(y) \; r_2(x) \; w_2(z) \; r_1(x) \; w_1(y) \; w_1(x) \\ H[s](x) = H_s[w_1(x)] = f_{1x}(f_{0x}(\cdot)) = H_{s'}[w_1(x)] = H[s'](x) \\ H[s](y) = H_s[w_1(y)] = f_{1y}(f_{0x}(\cdot)) = H_{s'}[w_1(y)] = H[s'](y) \\ H[s](z) = H_s[w_2(z)] = f_{2z}(f_{0x}(\cdot), f_{0y}(\cdot)) = H_{s'}[w_2(z)] = H[s'](z) \end{array} \right) \Rightarrow \; s \approx_f s$$

Example b:

$$\begin{array}{l} \text{Sample Si} \\ \text{s} = \mathbf{r}_1(\mathbf{x}) \ \mathbf{r}_2(\mathbf{y}) \ \mathbf{w}_1(\mathbf{y}) \ \mathbf{w}_2(\mathbf{y}) \\ \text{s'} = \mathbf{r}_1(\mathbf{x}) \ \mathbf{w}_1(\mathbf{y}) \ \mathbf{r}_2(\mathbf{y}) \ \mathbf{w}_2(\mathbf{y}) \\ \text{H[s]}(\mathbf{y}) = \mathbf{H}_{\mathbf{s}}[\mathbf{w}_2(\mathbf{y})] = \mathbf{f}_{2\mathbf{y}}(\mathbf{f}_{0\mathbf{y}}(\)) \\ \text{H[s']}(\mathbf{y}) = \mathbf{H}_{\mathbf{s'}}[\mathbf{w}_2(\mathbf{y})] = \mathbf{f}_{2\mathbf{y}}(\mathbf{f}_{1\mathbf{y}}(\mathbf{f}_{0\mathbf{x}}(\))) \end{array}$$

Reads-from Relation

Definition 3.7 (Reads-from Relation; Useful, Alive, and Dead Steps):

Given a schedule s, extended with an initial and a final transaction, t_0 and t_{∞} .

- (i) $\mathbf{r_j}(\mathbf{x})$ reads \mathbf{x} in \mathbf{s} from $\mathbf{w_i}(\mathbf{x})$ if $\mathbf{w_i}(\mathbf{x})$ is the last write on \mathbf{x} s.t. $\mathbf{w_i}(\mathbf{x}) \leq_{\mathbf{s}} \mathbf{r_j}(\mathbf{x})$.
- (ii) The reads-from relation of s is $RF(s) := \{(t_i, x, t_i) \mid \text{an } r_i(x) \text{ reads } x \text{ from a } w_i(x)\}.$
- (iii) Step p is **directly useful** for step q, denoted p → q, if q reads from p, or p is a read step and q is a subsequent write step of the same transaction.
 - → *, the "useful" relation, denotes the reflexive and transitive closure of →
- (iv) Step p is alive in s if it is useful for some step from t_{∞} , and dead otherwise.
- (v) The **live-reads-from relation** of s is $LRF(s) := \{(t_i, x, t_i) \mid \text{ an alive } r_i(x) \text{ reads } x \text{ from } w_i(x)\}$

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 LRF(s) := {(t_i, x, t_i) | an alive r_i(x) reads x from w_i(x)}

```
Example 3.7: s = r_1(x) \ r_2(y) \ w_1(y) \ w_2(y)
s' = r_1(x) \ w_1(y) \ r_2(y) \ w_2(y)
RF(s) = \{(t_0, x, t_1), (t_0, y, t_2), (t_0, x, t_{\infty}), (t_2, y, t_{\infty})\}
RF(s') = \{(t_0, x, t_1), (t_1, y, t_2), (t_0, x, t_{\infty}), (t_2, y, t_{\infty})\}
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```

Final-State Serializability

Theorem 3.1:

For schedules s and s' the following statements hold.

- (i) $s \approx s' \text{ iff op}(s) = op(s') \text{ and } LRF(s) = LRF(s').$
- (ii) For s let the step graph D(s)=(V,E) be a directed graph with vertices V:=op(s) and edges $E:=\{(p,q) \mid p \rightarrow q\}$, and the reduced step graph $D_1(s)$ be derived from D(s) by removing all vertices that correspond to dead steps. Then LRF(s)=LRF(s') iff $D_1(s)=D_1(s')$.

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Corollary 3.1:

Final-state equivalence of two schedules s and s' can be decided in time that is polynomial in the length of the two schedules.

Final-State Serializability

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- (i) $s \approx s' \text{ iff op}(s) = op(s') \text{ and } LRF(s) = LRF(s').$
- (ii) For s let the step graph D(s)=(V,E) be a directed graph with vertices V:=op(s) and edges E:={(p,q) | p→q}, and the reduced step graph D₁(s) be derived from D(s) by removing all vertices that correspond to dead steps. Then LRF(s)=LRF(s') iff D₁(s)=D₁(s').

Corollary 3.1:

Final-state equivalence of two schedules s and s' can be decided in time that is polynomial in the length of the two schedules.

Definition 3.8 (Final State Serializability):

A schedule s is **final state serializable** if there is a serial schedule s' s.t. s \approx s'. FSR denotes the class of all final-state serializable histories.

FSR: Example 3.9

$$s = r_{1}(x) r_{2}(y) w_{1}(y) w_{2}(y)$$

$$s' = r_{1}(x) w_{1}(y) r_{2}(y) w_{2}(y)$$

$$D(s):$$

$$w_{0}(x) w_{0}(y)$$

$$r_{2}(y)$$

$$r_{1}(x) w_{1}(y)$$

$$r_{2}(y)$$

steps

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Canonical Anomalies Reconsidered

Lost update anomaly:

$$L = r_1(x) r_2(x) w_1(x) w_2(x) c_1 c_2$$

⇒ history is not FSR $LRF(L) = \{(t_0, x, t_2), (t_2, x, t_\infty)\}$ $LRF(t_1, t_2) = \{(t_0, x, t_1), (t_1, x, t_2), (t_2, x, t_\infty)\}$ $LRF(t_2, t_1) = \{(t_0, x, t_2), (t_2, x, t_1), (t_1, x, t_\infty)\}$

• Inconsistent read anomaly:

$$I = r_2(x) w_2(x) r_1(x) r_1(y) r_2(y) w_2(y) c_1 c_2$$

→ history is FSR ! $\begin{array}{l} LRF(I) = \{(t_0,x,t_2),\,(t_0,y,t_2),\,(t_2,x,t_\infty),\,(t_2,y,t_\infty)\} \\ LRF(t_1\,\,t_2) = \{(t_0,x,t_2),\,(t_0,y,t_2),\,(t_2,x,t_\infty),\,(t_2,y,t_\infty)\} \\ LRF(t_2\,\,t_1) = \{(t_0,x,t_2),\,(t_0,y,t_2),\,(t_2,x,t_\infty),\,(t_2,y,t_\infty)\} \end{array}$

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Observation: (Herbrand) semantics of all read steps matters!

View Serializability

Definition 3.9 (View Equivalence):

Schedules s and s' are **view equivalent**, denoted $s \approx_v s'$, if the following hold:

- (i) op(s)=op(s')
- (ii) H[s] = H[s']
- (iii) $H_s[p] = H_{s'}[p]$ for all (read or write) steps

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Theorem 3.2:

For schedules s and s' the following statements hold.

- (i) $s \approx s' \text{ iff op}(s) = op(s') \text{ and } RF(s) = RF(s')$
- (ii) $s \approx s' \text{ iff } D(s) = D(s')$

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Corollary 3.2:

View equivalence of two schedules s and s' can be decided in time that is polynomial in the length of the two schedules.

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View equivalence of two schedules s and s' can be decided in time that is polynomial in the length of the two schedules.

Definition 3.10 (View Serializability):

A schedule s is view serializable if there exists a serial schedule s' s.t. s \approx_v s'. VSR denotes the class of all view-serializable histories.

Inconsistent Read Reconsidered

• Inconsistent read anomaly:

$$I = r_2(x) w_2(x) r_1(x) r_1(y) r_2(y) w_2(y) c_1 c_2$$

→ history is not VSR!

$$\begin{split} &RF(I) = \{(t_0,x,t_2),\,(t_2,x,t_1),\,(t_0,y,t_1),\,(t_0,y,t_2),\,(t_2,x,t_\infty),\,(t_2,y,t_\infty)\}\\ &RF(t_1\,\,t_2) = \{(t_0,x,t_1),\,(t_0,y,t_1),\,(t_0,x,t_2),\,(t_0,y,t_2),\,(t_2,x,t_\infty),\,(t_2,y,t_\infty)\}\\ &RF(t_2\,\,t_1) = \{(t_0,x,t_2),\,(t_0,y,t_2),\,(t_2,x,t_1),\,(t_2,y,t_1),\,(t_2,x,t_\infty),\,(t_2,y,t_\infty)\} \end{split}$$

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Observation: VSR properly captures our intuition

Relationship Between VSR and FSR

Theorem 3.3:

 $VSR \subset FSR$.

Theorem 3.4:

Let s be a history without dead steps. Then $s \in VSR$ iff $s \in FSR$.

On the Complexity of Testing VSR

Theorem 3.5:

The problem of deciding for a given schedule s whether $s \in VSR$ holds is NP-complete.

Properties of VSR

Definition 3.11 (Monotone Classes of Histories)

Let s be a schedule and $T \subseteq trans(s)$. $\Pi_T(s)$ denotes the projection of s onto T. A class E of histories is called monotone if the following holds:

if s is in E, then $\Pi_T(s)$ is in E for each $T \subseteq trans(s)$.

VSR is not monotone.

Example:

$$\begin{split} s &= w_1(x) \ w_2(x) \ w_2(y) \ c_2 \ w_1(y) \ c_1 \ w_3(x) \ w_3(y) \ c_3 \\ \Pi_{\{t1, t2\}}(s) &= w_1(x) \ w_2(x) \ w_2(y) \ c_2 \ w_1(y) \ c_1 \end{split}$$

 \rightarrow \in VSR

→ ∉ VSR

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Definition 3.12 (Conflicts and Conflict Relations):

Let s be a schedule, $t, t' \in trans(s), t \neq t'$.

- (i) Two data operations $p \in t$ and $q \in t'$ are in **conflict** in s if they access the same data item and at least one of them is a write.
- (ii) $\{(p,q)\} \mid p, q \text{ are in conflict and } p \leq_s q \}$ is the **conflict relation** of s.

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Schedule s is **conflict serializable** if there is a serial schedule s' s.t. $s \approx_c s'$. CSR denotes the class of all conflict serializable schedules.

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Example a: $r_1(x) r_2(x) r_1(z) w_1(x) w_2(y) r_3(z) w_3(y) c_1 c_2 w_3(z) c_3$

 $\rightarrow \in CSR$

Example b: $r_2(x) w_2(x) r_1(x) r_1(y) r_2(y) w_2(y) c_1 c_2$

→ ∉ CSR

Properties of CSR

Theorem 3.8:

 $CSR \subset VSR$

Example: $s = w_1(x) w_2(x) w_2(y) c_2 w_1(y) c_1 w_3(x) w_3(y) c_3 s \in VSR$, but $s \notin CSR$.

Theorem 3.9:

- (i) CSR is monotone.
- (ii) $s \in CSR \Leftrightarrow \Pi_T(s) \in VSR$ for all $T \subseteq trans(s)$ (i.e., CSR is the largest monotone subset of VSR).

Activity

• What is a directed graph?

• Think of ways to associate a graph with a schedule!

Conflict Graph

Definition 3.15 (Conflict Graph):

Let s be a schedule. The **conflict graph** G(s) = (V, E) is a directed graph with vertices V := commit(s) and edges $E := \{(t, t') \mid t \neq t' \text{ and there are steps } p \in t, q \in t' \text{ with } (p, q) \in \text{conf}(s) \}$.

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Theorem 3.10:

Let s be a schedule. Then $s \in CSR$ iff G(s) is acyclic.

Corollary 3.4:

Testing if a schedule is in CSR can be done in time polynomial to the schedule's number of transactions.

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Corollary 3.4:

Testing if a schedule is in CSR can be done in time polynomial to the schedule's number of transactions.

Example 3.12:

$$s = r_1(y) r_3(w) r_2(y) w_1(y) w_1(x) w_2(x) w_2(z) w_3(x) c_1 c_3 c_2$$

$$G(s): t1 \xrightarrow{t_3} t2$$

Activity

• What is a characterization (in a mathematical sense)?

 How do you prove a necessary and sufficient condition?

• What needs to be shown for the serializability theorem?

Proof of the Conflict-Graph Theorem

(i) Let s be a schedule in CSR. So there is a serial schedule s' with conf(s) = conf(s'). Now assume that G(s) has a cycle t₁ → t₂ → ... → tk → t₁.
This implies that there are pairs (p₁, q₂), (p₂, q₃), ..., (pk, q₁) with pᵢ ∈ tᵢ, qᵢ ∈ tᵢ, pᵢ <₅ q(i+1), and pᵢ in conflict with q(i+1). Because s' ≈ s, it also implies that pᵢ <₅' q(i+1).
Because s' is serial, we obtain tᵢ <₅' t(i+1) for i=1, ..., k-1, and tk <₅' t₁. By transitivity we infer t₁ <₅' t₂ and t₂ <₅' t₁, which is impossible.
This contradiction shows that the initial assumption is wrong. So G(s) is acyclic.

(ii) Let G(s) be acyclic. So it must have at least one source node.

The following topological sort produces a total order < of transactions:

- a) start with a source node (i.e., a node without incoming edges),
- b) remove this node and all its outgoing edges,
- c) iterate a) and b) until all nodes have been added to the sorted list.

The total transaction ordering order \leq preserves the edges in G(s); therefore it yields a serial schedule s' for which s' \approx s.

Commutativity and Ordering Rules

Commutativity rules:

C1: $r_i(x) r_j(y) \sim r_j(y) r_i(x)$ if $i \neq j$ C2: $r_i(x) w_j(y) \sim w_j(y) r_i(x)$ if $i \neq j$ and $x \neq y$ C3: $w_i(x) w_i(y) \sim w_i(y) w_i(x)$ if $i \neq j$ and $x \neq y$

Ordering rule:

C4: $o_i(x)$, $p_j(y)$ unordered $\sim > o_i(x)$ $p_j(y)$ if $x \neq y$ or both o and p are reads

Example for transformations of schedules:

$$\begin{array}{lll} s & = w_1(x) \underbrace{r_2(x) \ w_1(y) \ w_1(z) \ r_3(z) \ w_2(y) \ w_3(y) \ w_3(z)}_{} \\ \sim> & [C2] & w_1(x) \ w_1(y) \underbrace{r_2(x) \ w_1(z) \ w_2(y) \ r_3(z) \ w_3(y) \ w_3(z)}_{} \\ \sim> & [C2] & w_1(x) \ w_1(y) \ w_1(z) \ r_2(x) \ w_2(y) \ r_3(z) \ w_3(y) \ w_3(z) \\ & = t_1 \ t_2 \ t_3 \end{array}$$

Commutativity-based Reducibility

Definition 3.16 (Commutativity Based Equivalence):

Schedules s and s' s.t. op(s)=op(s') are **commutativity based equivalent**, denoted s \sim * s', if s can be transformed into s' by applying rules C1, C2, C3, C4 finitely many times.

Theorem 3.11:

Let s and s' be schedules s.t. op(s)=op(s'). Then s \approx s' iff s \sim * s'.

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Theorem 3.11:

Let s and s' be schedules s.t. op(s)=op(s'). Then s \approx s' iff s \sim * s'.

Definition 3.17 (Commutativity Based Reducibility):

Schedule s is **commutativity-based reducible** if there is a serial schedule s' s.t. $s \sim s$ s'.

Corollary 3.5:

Schedule s is commutativity-based reducible iff $s \in CSR$.

Order Preserving Conflict Serializability

Definition 3.18 (Order Preservation):

Schedule s is order preserving conflict serializable if it is conflict equivalent to a serial schedule s' and for all t, $t' \in trans(s)$: if t completely precedes t' in s, then the same holds in s'. OCSR denotes the class of all schedules with this property.

Theorem 3.12:

 $OCSR \subset CSR$.

Example 3.13:

$$s = w_1(x) r_2(x) c_2 w_3(y) c_3 w_1(y) c_1$$

- \rightarrow \in CSR
- → ∉ OCSR

Commit-order Preserving Conflict Serializability

Definition 3.19 (Commit Order Preservation):

Schedule s is **commit order preserving conflict serializable** if for all t_i , $t_j \in \text{trans}(s)$: if there are $p \in t_i$, $q \in t_j$ with $(p,q) \in \text{conf}(s)$ then $c_i \leq_s c_j$. COCSR denotes the class of all schedules with this property.

Theorem 3.13: $COCSR \subseteq CSR$.

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Theorem 3.13:

 $COCSR \subset CSR$.

Theorem 3.14:

Schedule s is in COCSR iff there is a serial schedule s' s.t. s \approx_c s' and for all t_i , $t_i \in trans(s)$: $t_i <_{s'} t_i \Leftrightarrow c_i <_{s} c_i$.

Commit-order Preserving Conflict Serializability

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Theorem 3.13:

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Schedule s is in COCSR iff there is a serial schedule s' s.t. s \approx_c s' and for all t_i , $t_i \in trans(s)$: $t_i <_{s'} t_i \Leftrightarrow c_i <_s c_i$.

Theorem 3.15:

 $COCSR \subset OCSR$.

Example:

$$s = w_3(y) c_3 w_1(x) r_2(x) c_2 w_1(y) c_1$$

- \rightarrow \in OCSR
- → ∉ COCSR

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Commit Serializability

Definition 3.20 (Closure Properties of Schedule Classes):

Let E be a class of schedules.

For schedule s let CP(s) denote the projection $\Pi_{\text{commit}(s)}$ (s).

E is **prefix-closed** if the following holds: $s \in E \Leftrightarrow p \in E$ for each prefix of s.

E is **commit-closed** if the following holds: $s \in E \Rightarrow CP(s) \in E$.

Theorem 3.16:

CSR is prefix-commit-closed, i.e., prefix-closed and commit-closed.

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CSR is prefix-commit-closed, i.e., prefix-closed and commit-closed.

Definition 3.21 (Commit Serializability):

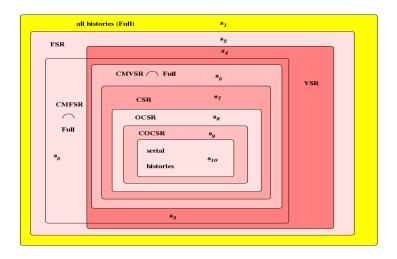
Schedule s is **commit-\Theta-serializable** if CP(p) is Θ -serializable for each prefix p of s, where Θ can be FSR, VSR, or CSR.

The resulting classes of commit- Θ -serializable schedules are denoted CMFSR, CMVSR, and CMCSR.

Theorem 3.17:

- (i) CMFSR, CMVSR, CMCSR are prefix -commit-closed.
- (ii) $CMCSR \subset CMVSR \subset CMFSR$

Landscape of History Classes



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Interleaving Specifications: Motivation

Example: all transactions known in advance

transfer transactions on checking accounts a and b and savings account c:

$$t_1 = r_1(a) w_1(a) r_1(c) w_1(c)$$

 $t_2 = r_2(b) w_2(b) r_2(c) w_2(c)$

balance transaction:

$$t_3 = r_3(a) r_3(b) r_3(c)$$

audit transaction:

$$t_4 = r_4(a) r_4(b) r_4(c) w_4(z)$$

Possible schedules:

$$\begin{array}{l} r_1(a) \ w_1(a) \ r_2(b) \ w_2(b) \ r_2(c) \ w_2(c) \ r_1(c) \ w_1(c) \\ r_1(a) \ w_1(a) \ r_3(a) \ r_3(b) \ r_3(c) \ r_1(c) \ w_1(c) \\ r_1(a) \ w_1(a) \ r_2(b) \ w_2(b) \ r_1(c) \ r_2(c) \ w_2(c) \ w_1(c) \\ r_1(a) \ w_1(a) \ r_4(a) \ r_4(b) \ r_4(c) \ w_4(z) \ r_1(c) \ w_1(c) \end{array}$$

→ ∉ CSR

interleavings

Interleaving Specifications: Motivation

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transfer transactions on checking accounts a and b and savings account c:

$$t_1 = r_1(a) w_1(a) r_1(c) w_1(c)$$

 $t_2 = r_2(b) w_2(b) r_2(c) w_2(c)$

balance transaction:

$$t_3 = r_3(a) r_3(b) r_3(c)$$

audit transaction:

$$t_4 = r_4(a) r_4(b) r_4(c) w_4(z)$$

Possible schedules:

$$\begin{array}{lll} r_{0}(a) & w_{1}(a) & r_{2}(b) & w_{2}(b) & r_{2}(c) & w_{2}(c) & r_{1}(c) & w_{1}(c) \\ r_{1}(a) & w_{1}(a) & r_{3}(a) & r_{3}(b) & r_{3}(c) & r_{1}(c) & w_{1}(c) \\ \end{array} \begin{array}{ll} \rightarrow \in CSR \\ \text{application-tolerable} \\ \text{interleavings} \\ r_{1}(a) & w_{1}(a) & r_{2}(b) & w_{2}(b) & r_{1}(c) & r_{2}(c) & w_{2}(c) & w_{1}(c) \\ \end{array} \begin{array}{ll} \rightarrow \notin CSR \\ \text{interleavings} \\ \text{non-admissable} \\ \rightarrow \notin CSR \\ \end{array}$$

Observations: application may tolerate non-CSR schedules a priori knowledge of all transactions impractical

Indivisible Units

Definition 3.22 (Indivisible Units):

Let $T=\{t_1, ..., t_n\}$ be a set of transactions. For $t_i, t_j \in T$, $t_i \not= t_j$, an **indivisible unit** of t_i relative to t_j is a sequence of consecutive steps of t_i s.t. no operations of t_j are allowed to interleave with this sequence.

 $IU(t_i, t_j)$ denotes the ordered sequence of indivisible units of t_i relative to t_j . $IU_k(t_i, t_j)$ denotes the k^{th} element of $IU(t_i, t_j)$.

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Example 3.14:

$$t_1 = r_1(x) w_1(x) w_1(z) r_1(y)$$

$$t_2 = r_2(y) w_2(y) r_2(x)$$

$$t_3 = w_3(x) w_3(y) w_3(z)$$

$$\begin{split} & IU(t_1,t_2) = \langle \, [r_1(x) \ w_1(x)], \, [\ w_1(z) \ r_1(y)] \, \rangle \\ & IU(t_1,t_3) = \langle \, [r_1(x) \ w_1(x)], \, [\ w_1(z)], \, [\ r_1(y)] \, \rangle \\ & IU(t_2,t_1) = \langle \, [r_2(y)], \, [\ w_2(y) \ r_2(x)] \, \rangle \\ & IU(t_2,t_3) = \langle \, [r_2(y) \ w_2(y)], \, [\ r_2(x)] \, \rangle \\ & IU(t_3,t_1) = \langle \, [w_3(x) \ w_3(y)], \, [w_3(z)] \, \rangle \\ & IU(t_3,t_2) = \langle \, [w_3(x) \ w_3(y)], \, [w_3(z)] \, \rangle \end{split}$$

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Let $T = \{t_1, ..., t_n\}$ be a set of transactions. For $t_i, t_j \in T$, $t_i \neq t_j$, an **indivisible unit** of t_i relative to t_j is a sequence of consecutive steps of t_i s.t. no operations of t_j are allowed to interleave with this sequence.

 $IU(t_i, t_j)$ denotes the ordered sequence of indivisible units of t_i relative to t_j . $IU_k(t_i, t_j)$ denotes the k^{th} element of $IU(t_i, t_j)$.

$$\begin{array}{lll} \textbf{Example 3.14:} & & & & & & & & & & & & & & & & & \\ \textbf{t}_1 = \textbf{r}_1(\textbf{x}) \ \textbf{w}_1(\textbf{x}) \ \textbf{w}_1(\textbf{z}) \ \textbf{r}_1(\textbf{y}) & & & & & & & & & \\ \textbf{t}_1 = \textbf{r}_1(\textbf{x}) \ \textbf{w}_1(\textbf{x}) \ \textbf{w}_1(\textbf{z}) \ \textbf{r}_1(\textbf{y}) & & & & & & & \\ \textbf{IU}(\textbf{t}_1, \ \textbf{t}_3) = < [\textbf{r}_1(\textbf{x}) \ \textbf{w}_1(\textbf{x})], [\ \textbf{w}_1(\textbf{z})], [\ \textbf{r}_1(\textbf{y})] > \\ \textbf{t}_2 = \textbf{r}_2(\textbf{y}) \ \textbf{w}_2(\textbf{y}) \ \textbf{r}_2(\textbf{x}) & & & & & & & \\ \textbf{IU}(\textbf{t}_2, \ \textbf{t}_1) = < [\textbf{r}_2(\textbf{y})], [\ \textbf{w}_2(\textbf{y}) \ \textbf{r}_2(\textbf{x})] > \\ \textbf{IU}(\textbf{t}_2, \ \textbf{t}_3) = < [\textbf{r}_2(\textbf{y}) \ \textbf{w}_2(\textbf{y})], [\ \textbf{w}_3(\textbf{z})] > \\ \textbf{IU}(\textbf{t}_3, \ \textbf{t}_1) = < [\textbf{w}_3(\textbf{x}) \ \textbf{w}_3(\textbf{y})], [\ \textbf{w}_3(\textbf{z})] > \\ \textbf{IU}(\textbf{t}_3, \ \textbf{t}_2) = < [\textbf{w}_3(\textbf{x}) \ \textbf{w}_3(\textbf{y})], [\ \textbf{w}_3(\textbf{z})] > \\ \end{array}$$

Example 3.15:

Relatively Serializable Schedules

Definition 3.23 (Dependence of Steps):

Step q directly **depends on** step p in schedule s, denoted $p \sim p$, if $p <_s q$ and either p, q belong to the same transaction t and $p <_t q$ or p and q are in conflict. $p \sim p$ denotes the reflexive and transitive closure of $p \sim p$.

Relatively Serializable Schedules

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Definition 3.24 (Relatively Serial Schedule):

s is **relatively serial** if for all transactions t_i , t_j : if $q \in t_j$ is interleaved with some $IU_k(t_i, t_j)$, then there is no operation $p \in IU_k(t_i, t_j)$ s.t. p > *q or q > *p

Example 3.16:

$$s_3 = r_1(x) r_2(y) w_1(x) w_2(y) w_3(x) w_1(z) w_3(y) r_2(x) r_1(y) w_3(z)$$

Relatively Serializable Schedules

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Definition 3.25 (Relatively Serializable Schedule):

s is **relatively serializable** if it is conflict equivalent to a relatively serial schedule.

Example 3.17:

$$s_4 = r_1(x) r_2(y) w_2(y) w_1(x) w_3(x) r_2(x) w_1(z) w_3(y) r_1(y) w_3(z)$$

Relative Serialization Graph

Definition 3.26 (Push Forward and Pull Backward):

Let $IU_k(t_i, t_j)$ be an IU of t_i relative to t_j . For an operation $p_i \in IU_k(t_i, t_j)$ let

- (i) $F(p_i, t_i)$ be the last operation in $IU_k(t_i, t_i)$ and
- (ii) $B(p_i, t_i)$ be the first operation in $IU_k(t_i, t_i)$.

Definition 3.27 (Relative Serialization Graph):

The relative serialization graph RSG(s) = (V,E) of schedule s is a graph with vertices V := op(s) and edge set $E \subseteq V \cdot V$ containing four types of edges:

- (i) for consecutive operations p, q of the same transaction $(p, q) \in E$ (*I-edge*)
- (ii) if $p \sim p \in t_i$, $q \in t_j$, $t_i \not= t_j$, then $(p, q) \in E$ (D-edge)
- (iii) if (p, q) is a D-edge with $p \in t_i$, $q \in t_j$, then $(F(p, t_j), q) \in E$ (F-edge)
- $\text{(iv)} \ \ \text{if } (p,q \) \ \text{is a D-edge with } p \in t_i, \ q \in t_j, \ \text{then } (p, \ B(q, \ t_i)) \in E \\ \tag{B-edge}$

Theorem 3.18:

A schedule s is relatively serializable iff RSG(s) is acyclic.

RSG Example

Example 3.19:

$$\mathbf{t}_1 = \mathbf{w}_1(\mathbf{x}) \; \mathbf{r}_1(\mathbf{z})$$

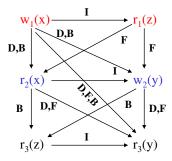
$$t_2 = r_2(x) w_2(y)$$

$$t_3 = r_3(z) r_3(y)$$

$$s_5 = w_1(x) r_2(x) r_3(z) w_2(y) r_3(y) r_1(z)$$

$$\begin{split} & IU(t_1, t_2) = \langle \left[w_1(x) \, r_1(z) \right] \rangle \\ & IU(t_1, t_3) = \langle \left[w_1(x) \right], \left[\quad r_1(z) \right] \rangle \\ & IU(t_2, t_1) = \langle \left[r_2(x) \right], \left[\quad w_2(y) \right] \rangle \\ & IU(t_2, t_3) = \langle \left[r_2(x) \right], \left[\quad w_2(y) \right] \rangle \\ & IU(t_3, t_1) = \langle \left[r_3(z) \right], \left[r \quad _3(y) \right] \rangle \\ & IU(t_3, t_2) = \langle \left[r_3(z) \, r_3(y) \right] \rangle \end{split}$$

$RSG(s_5)$:



Chapter 3: Concurrency Control – Notions of Correctness for the Page Model

- 3.2 Canonical Synchronization Problems
- 3.3 Syntax of Histories and Schedules
- 3.4 Correctness of Histories and Schedules
- 3.5 Herbrand Semantics of Schedules
- 3.6 Final-State Serializability
- 3.7 View Serializability
- 3.8 Conflict Serializability
- 3.9 Commit Serializability
- 3.10 An Alternative Criterion: Interleaving Specifications
- 3.11 Lessons Learned

Lessons Learned

- Equivalence to serial history is a natural correctness criterion
- CSR, albeit less general than VSR,
 - is most appropriate for
 - complexity reasons
 - its monotonicity property
 - its generalizability to semantically rich operations
- OCSR and COCSR have additional beneficial properties